FIG. 1

LVC114.H

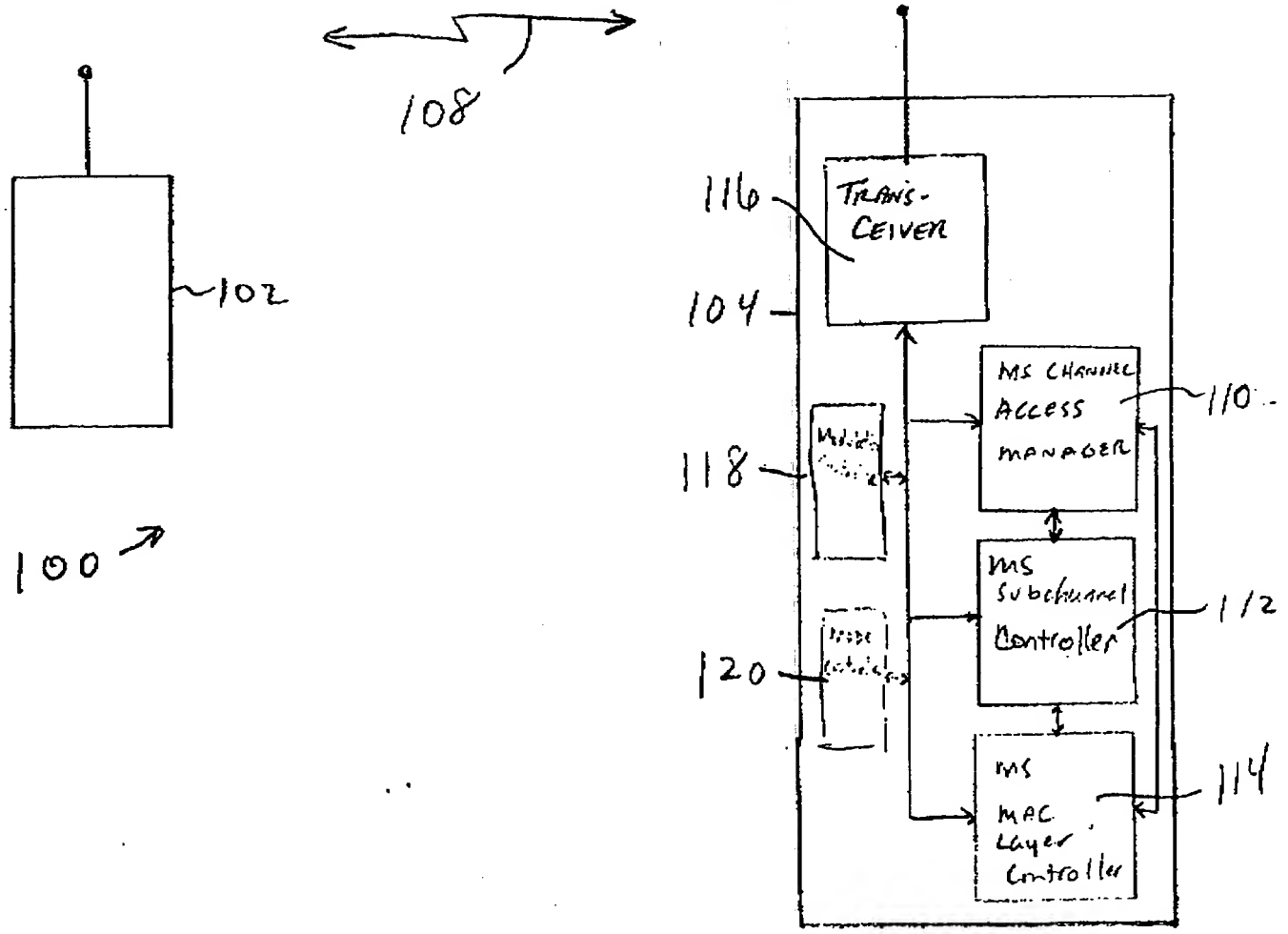
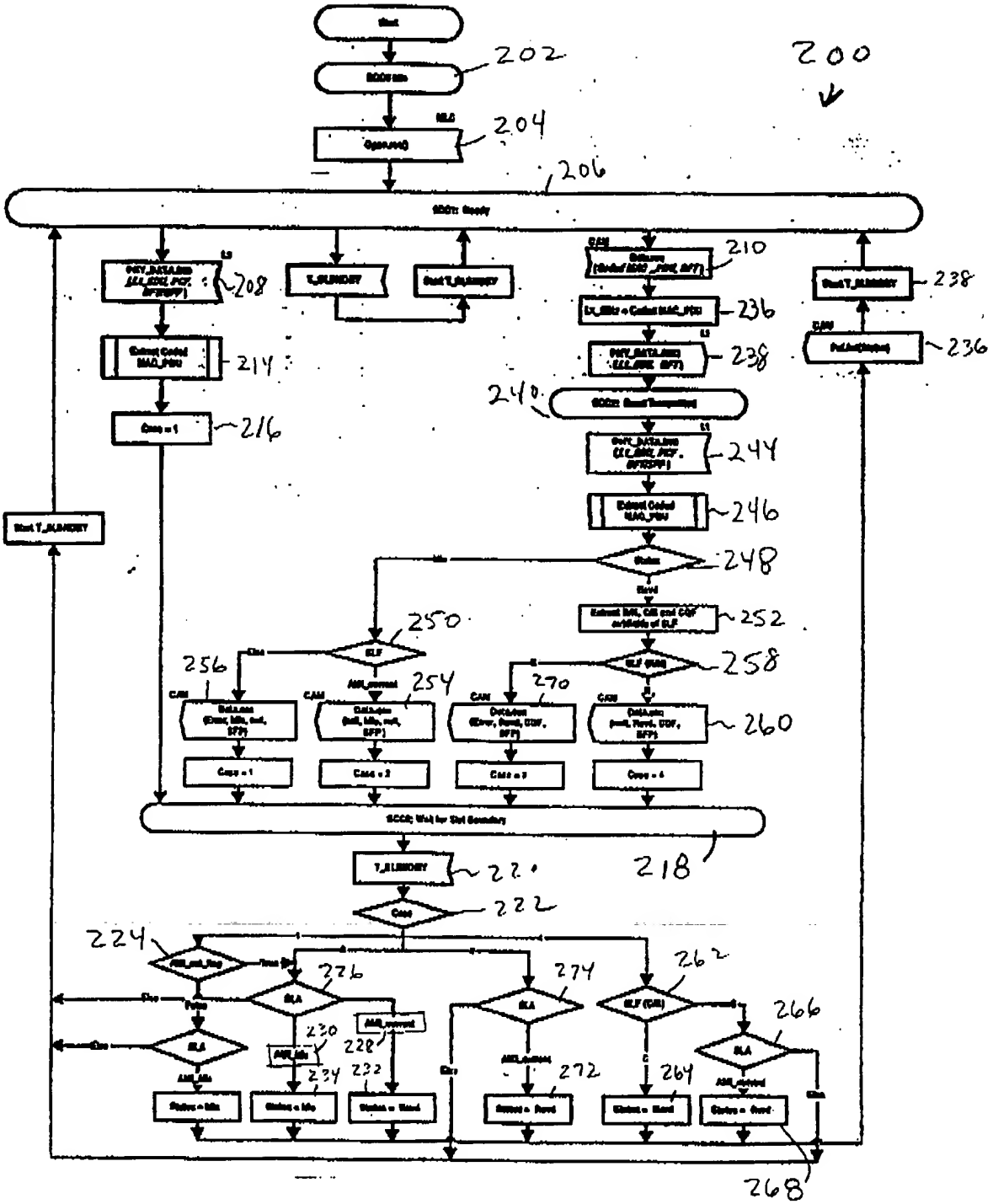


FIG. 2A

LVC 114 H



LVC 114H

FIG. 2B

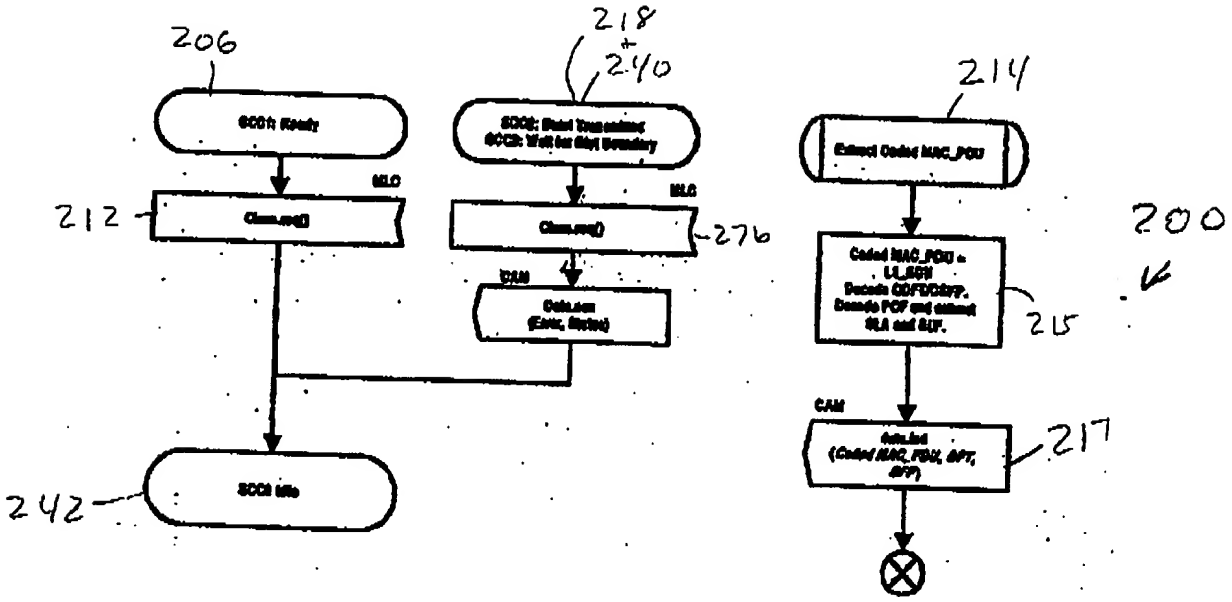


FIG.3

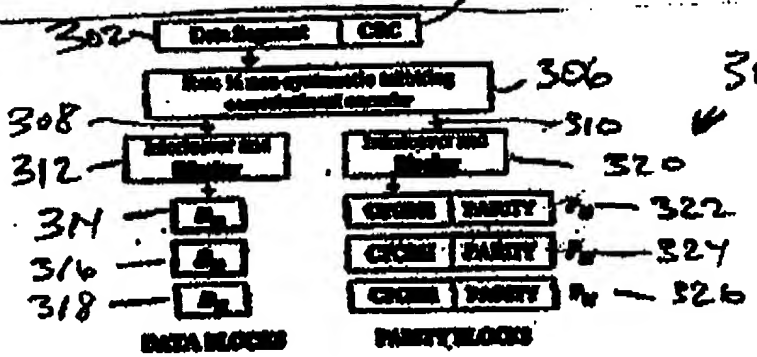


FIG.4

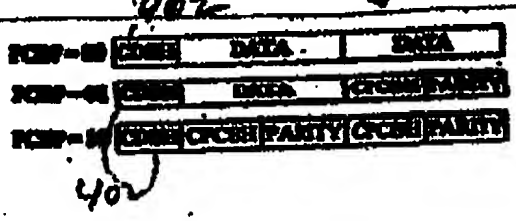


FIG.5

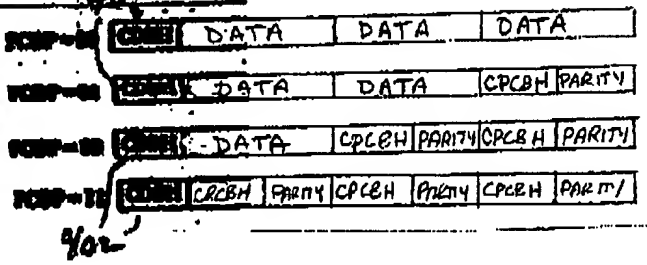


FIG.6

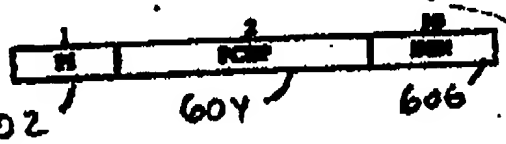


FIG.7

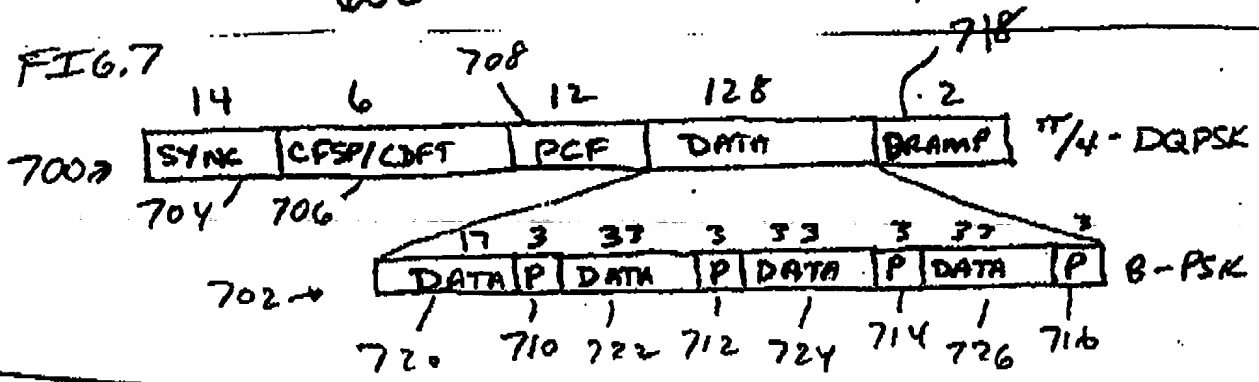
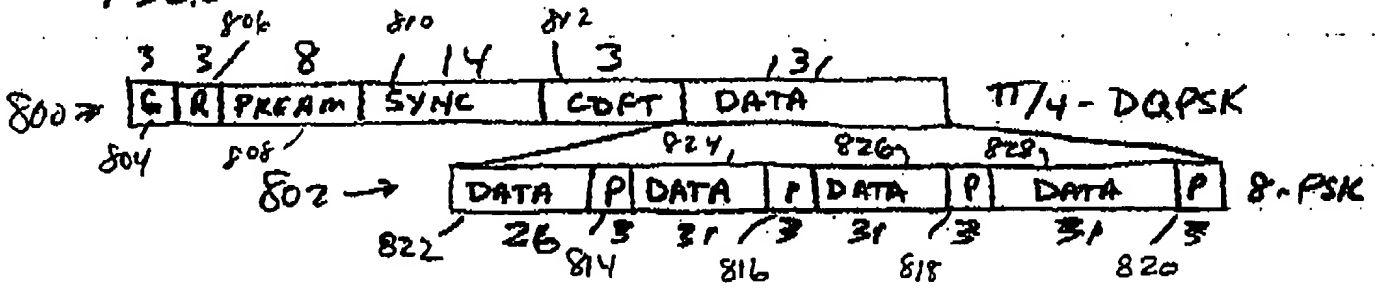


FIG.8



LUC.114H

FIG9.

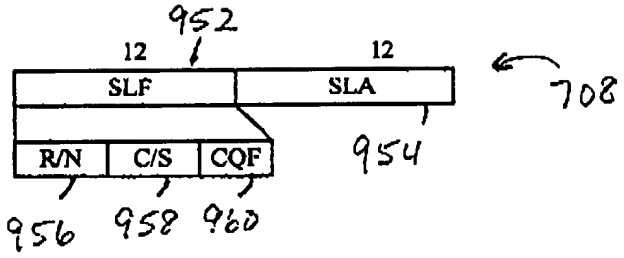
Direction	Modulation	Number of 11 octet RLP segments	PDU size (bits)	Rate ½ punctured convolutional encoder output (bits)	Peak triple rate throughput (kb/s)
Downlink	π/4-DQPSK	2	205	256	26.4
	8-PSK	3	293	348	39.6
	16-level	4	381	512	52.8
Uplink	π/4-DQPSK	2	205	262	26.4
	8-PSK	3	293	357	39.6
	16-level	4	381	524	52.8
Uplink (abbreviated)	π/4-DQPSK	2	173	218	21.6
	8-PSK	3	245	291	32.4
	16-level	4	317	436	43.2

900 ↗

FIG. 10

950 ↘

Direction	RLP Segment size (octets)	CRC size (bits)	RLP block size (bits)	Modulation	Number of RLP blocks per PDU	CDSH size (bits)	PDU size (bits)	Peak triple rate throughput (kb/s)
Downlink	37	16	104	π/4-DQPSK	2	48	256	29.6
				8-PSK	3	36	348	44.4
				16-level	4	96	512	59.2
Uplink	38	14	106	π/4-DQPSK	2	50	262	29.7
				8-PSK	3	39	357	45.6
				16-level	4	100	524	59.4
Uplink (abbreviated)	30	15	85	π/4-DQPSK	2	48	218	24
				8-PSK	3	36	327	36
				16-level	4	96	436	48

FIG. 11

LUC-114H